

Name:

Joseph S Severino, Jr

Names of all higher education institutions attended, with degrees earned:

Austin College, B.A. in Mathematics

Texas Tech University, M.S. in Mathematics

All previous teaching positions, including the names of the institutions, the position, and beginning and ending dates of employment:

Texas Tech University, Graduate Teaching Assistant, September 1998 to May 2001

Midland College, Adjunct Mathematics Instructor, January 2002 to August 2004

Significant professional publications related to the teaching position, with a full citation for each:

Joseph Severino, Acceleration of Quasi-Monte Carlo Acceleration, Texas Tech University, 2001.

J. S. Severino, E. J. Allen, H. D. Victory, Jr., Acceleration of quasi-Monte Carlo approximations with applications in mathematical finance, Applied Mathematics and Computation, 148, 173-187 (2004).